

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L. L. P.
1300 I STREET, N. W.
WASHINGTON, DC 20005-3315

202 • 408 • 4000
FACSIMILE 202 • 408 • 4400

WRITER'S DIRECT DIAL NUMBER:

(202) 408-4024

December 20, 2000

Assistant Commissioner for Patents
Washington, D.C. 20231

Re: New U.S. Patent Application Entitled
TRANSISTOR STRUCTURE FOR
ELECTROSTATIC DISCHARGE
PROTECTION CIRCUIT
Inventor: Shi-Tron Lin
Attorney Docket No.: 06484.0073

Sir:

We enclose the following papers for filing in the U.S. Patent and Trademark Office in connection with the above-identified patent application. Please accord this application a serial number and filing date.

XX Application consisting of 43 pages, including 14 independent claims and 100 claims total.

XX Drawings consisting of 10 sheets of informal drawings with 23 figures.

XX Executed Declaration and Power of Attorney.

— An executed Declaration and Power of Attorney will be filed after the issuance of a Notice to File Missing Parts, along with the necessary filing and late fees.

XX Executed Assignment to Winbond Electronics Corporation and Recordation Form Cover Sheet. Please record and return the Assignment to the undersigned.

XX The filing fee is calculated on the basis of the claims as follows:


12/20/00
jc772 U.S. PTO

ATLANTA
404•653•6400
PALO ALTO
650•849•6600

JC853 U.S. PTO
09/14/00
12/20/00

TOKYO
011•813•3431•6943
BRUSSELS
011•322•646•0353

09740017 122000


Richard V. Burgujian
Registration No. 31,744